

GOVERNMENT OF INDIA  
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY  
**RAJYA SABHA**  
**UNSTARRED QUESTION NO. 552**  
TO BE ANSWERED ON: 7.2.2025

**STATUS OF SEMICON INDIA PROGRAMME**

**552# DR. KALPANA SAINI:**  
**SMT. KIRAN CHOUDHRY:**  
**SHRI NARHARI AMIN:**  
**SMT. MAYA NAROLIYA:**  
**SHRI LAHAR SINGH SIROYA:**

Will the Minister of Electronics and Information Technology be pleased to state:

- (a) the number of semiconductor manufacturing units approved under the Semicon India Programme; and
- (b) the investment made on each of these units and the production capacity thereof?

**ANSWER**

MINISTER OF STATE FOR ELECTRONICS AND INFORMATION TECHNOLOGY  
(SHRI JITIN PRASADA)

(a) and (b): Government has approved Semicon India programme with a total outlay of Rs 76,000 crore for the development of semiconductor and display manufacturing ecosystem in the country. The following projects have been approved under the programme:

- i. Micron Technology Inc.'s proposal for setting up an Assembly, Testing, Marking, and Packaging (ATMP) facility with an investment of Rs. 22,516 crore for memory products with a production capacity of around 40 Million Units per week.
- ii. Tata Electronics Private Limited (TEPL)'s proposal for setting up a Semiconductor Fab facility with an investment of Rs. 91,526 crore with production capacity of around 50,000 wafer starts per month (WSPM) in technology partnership with Powerchip Semiconductor Manufacturing Corporation (PSMC), Taiwan.
- iii. Tata Electronics Private Limited (TEPL)'s proposal for setting up of OSAT facility with an investment of Rs. 27,120 crores with a production capacity of 48 Million Units per day.
- iv. CG Power and Industrial Solutions Limited's proposal for setting up Outsourced Semiconductor Assembly and Test (OSAT) facility with an investment of Rs 7,584 crore with production capacity of around 15.07 Million Units per day.
- v. Kaynes Technology India Limited (KTIL) proposal for setting up of Outsourced Semiconductor Assembly and Test (OSAT) facility with an investment of Rs 3,307 crore with a production capacity of around 6.33 Million Units per day.
- vi. Seventeen (17) applications have been approved under Design Linked incentive Scheme to design chips for Indian products.

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